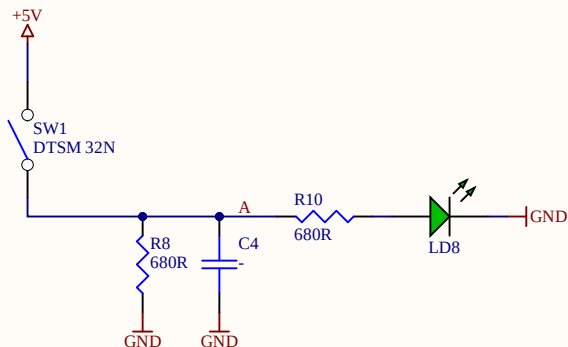
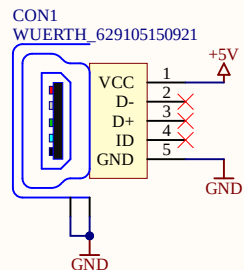
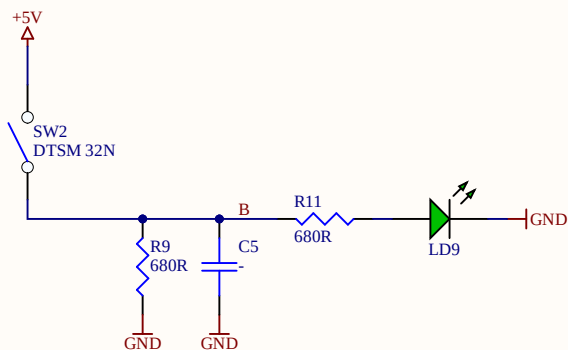


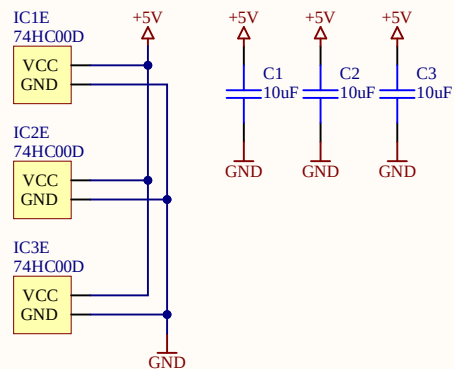
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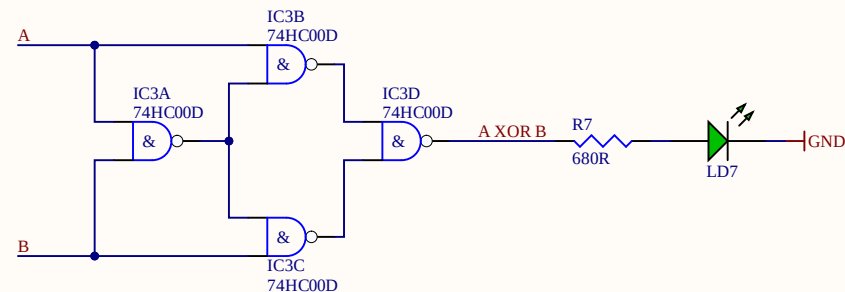
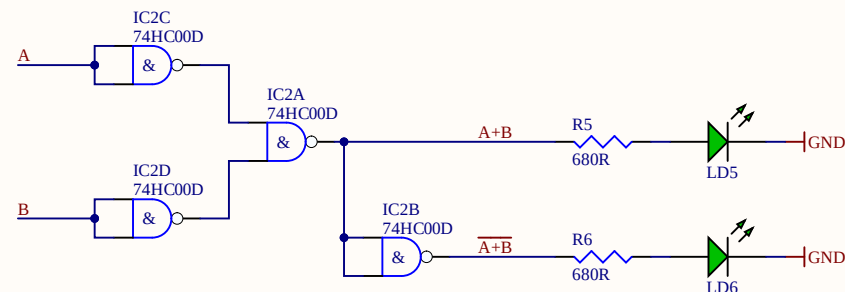
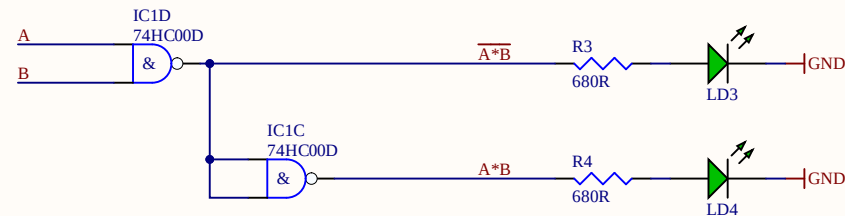
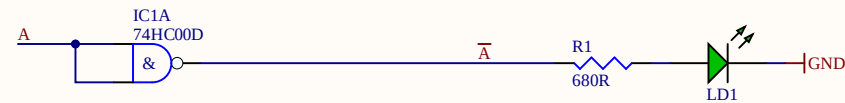
B



C



D

Title: **Mainpage**Project: **IN_PCB_RULER**Board: **Logic demo**Version: **V1p0**

Author: Szabó Mihály

SVN: Unknown revision

Sheet: 1 of 1

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